

INFORMATION DISCLOSURE STATEMENT PURSUANT TO
37 C.F.R. §§1.97-1.99

PATENT APPLICATION

Applicant(s): Kerim Kalafala *et al.*

Docket No.: END920030067US1

FOR: AFFINITY-BASED CLUSTERING OF VECTORS FOR PARTITIONING THE COLUMNS OF A MATRIX

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. §1.56 and pursuant to 37 C.F.R. §§1.97-1.99, Applicant(s) hereby notifies the U.S. Patent and Trademark Office of the documents listed on the attached Form PTO-1449. Applicant respectfully submits that all pending claims are patentable over the foregoing references, alone or in combination. The Examiner is requested to initial the enclosed Form PTO-1449 and return a copy thereof to the undersigned.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant reserves the right to dispute any of the listed documents as prior art during examination. Further, Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. Furthermore, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist. This Information Disclosure Statement is being filed within three months of the filing date of the captioned patent application, and therefore no certification under 37 C.F.R. §1.97(e) or fee under 37 C.F.R. §1.17(p) is required.

Respectfully submitted,

Dated: 10/03/2003

By Jack P. Friedman
Jack P. Friedman
Reg. No. 44,688

Enclosures: PTO-1449

INFORMATION DISCLOSURE CITATION*(Use several sheets if necessary)*ATTY DOCKET NO.
END920030067US1

SERIAL NO.

Kerim Kalafala et al.

FILING

GROUP

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE IF APPROPRIATE |
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| | | IBM Technical Disclosure Bulletin, Vol. 19, No. 11, April 1977, RANDOM DELAY SIMULATION, Savkar, pages 4257-4258. |
| | | Jess et al., STATISTICAL TIMING FOR PARAMETRIC YIELD PREDICTION OF DIGITAL INTEGRATED CIRCUITS, DAC 2003, June 2-6, 2003, pages 932-937. |

EXAMINER

DATE CONSIDERED

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